

L Number	Hits	Search Text	DB	Time stamp
3	36	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 10:16
4	2	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((address\$3 adj3 extension) near3 concatenat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 10:33
5	0	((address\$3 adj3 extension) near3 concatenat\$3) same ((another second) near3 (value width))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 10:38
6	0	((address\$3 adj3 extension) near3 concatenat\$3) same (address near3 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:20
7	0	((address\$2 adj3 signal) near3 width) same ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3)) same (address\$2 near3 extension near3 register signal)) and (sav\$3 retain\$3 stor\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:25
8	0	((address\$2 adj3 signal) near3 width) same ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:27
9	376	((address\$2 adj3 signal) near3 width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:27
11	0	((address\$2 adj3 signal) near3 width)) and ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:28
10	10	((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 12:28
-	36	(address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/12 10:13
-	36	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 10:56

-	32	(address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 10:56
-	32	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 12:32
-	26	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)) and ((local data pci) near3 bus\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 12:44
-	0	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)) and (pci near3 (device bus\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 12:45
-	0	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)) and (pci near7 (device bus\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 12:48

-	3	((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3 chang\$3 swap\$3 switch\$3) near5 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3 function\$3 initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3)) and (peripheral adj3 device) ("6292820" "6289369") .pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 12:49
-	4		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/09 13:41